Fig. 1 PRIOR ART

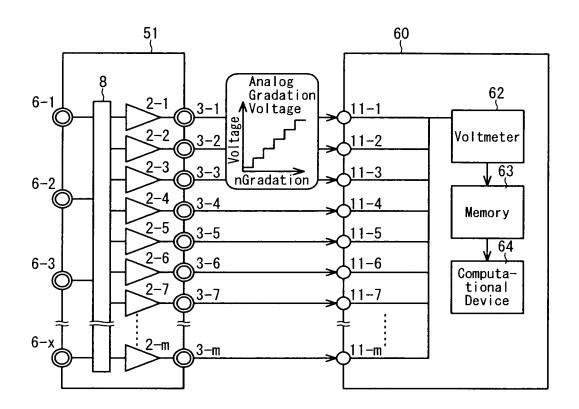


Fig. 2 PRIOR ART

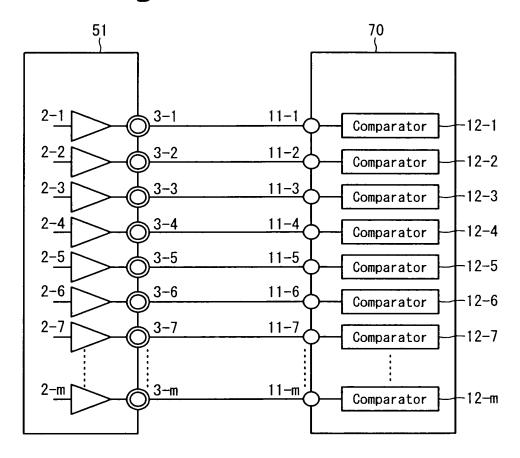


Fig. 3 PRIOR ART

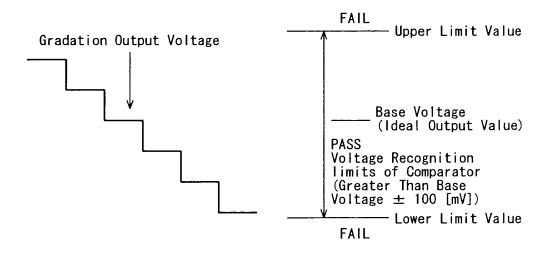


Fig. 4 PRIOR ART

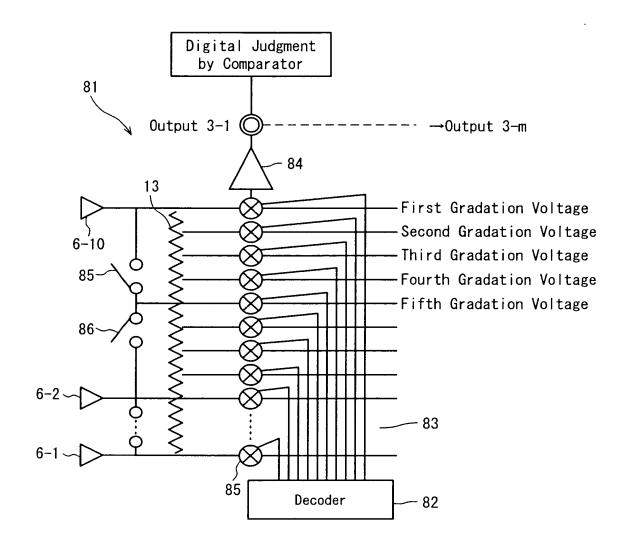


Fig. 5 PRIOR ART

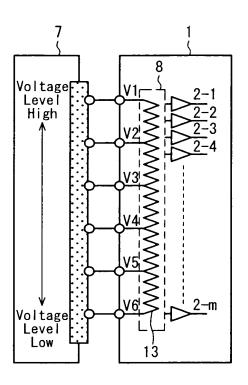
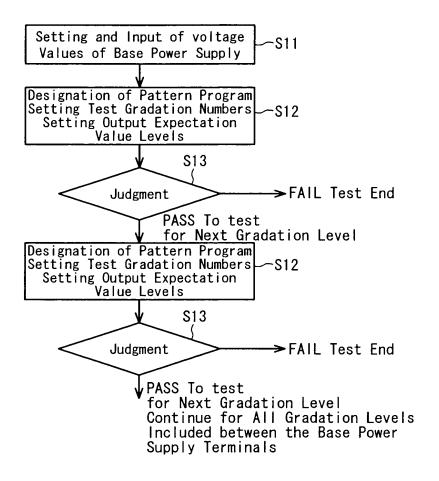


Fig. 6 PRIOR ART



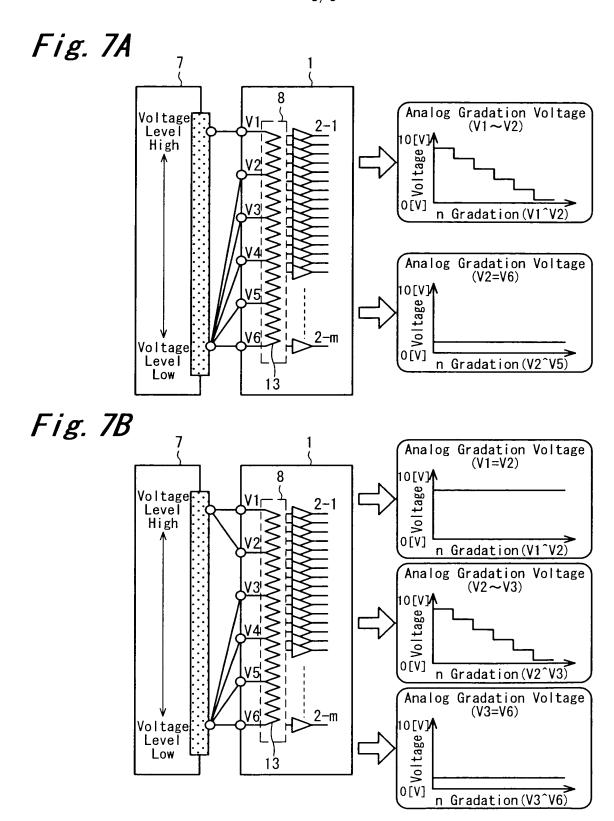


Fig. 8

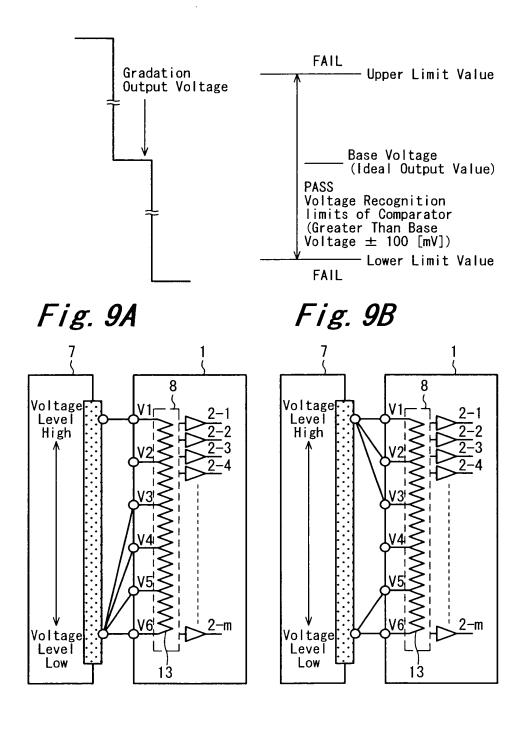


Fig. 10A

Fig. 10B

Voltage Level High V1 2-1 Level High V2 2-3 2-3 2-4 V5 V5 V5 V5 V5 V5 V5 V6 Level Low 13

Fig. 11

